

9-BIT COMPARATOR

FEATURES

- Max. propagation delay of 1500ps
- IEE min. of –120mA
- Industry standard 100K ECL levels
- Extended supply voltage option: VEE = -4.2V to -5.5V
- Voltage and temperature compensation for improved noise immunity
- Internal 75kΩ input pull-down resistors
- 120% faster than Fairchild
- Approximately 40% lower power than Fairchild
- Function and pinout compatible with Fairchild F100K
- Available in 28-pin PLCC packages

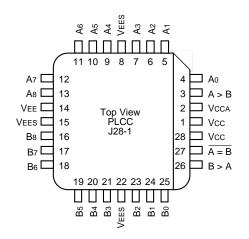
DESCRIPTION

The SY100S366 is an ultra-fast 9-bit magnitude comparator designed for use in high-performance ECL systems. The device compares the arithmetic value of two 9-bit words and indicates whether one word is greater than or equal to the other. The inputs on the device have $75k\Omega$ pull-down resistors.

PIN NAMES

Pin	Function
A0 – A8	A Data Inputs
B0 – B8	B Data Inputs
A > B	A Greater Than B Output
B > A	B Greater Than A Output
$\overline{A} = \overline{B}$	Complement A Equal to B Output (Active LOW)
VEES	VEE Substrate
VCCA	Vcco for ECL Outputs

PACKAGE/ORDERING INFORMATION



Ordering Information

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY100S366JC	J28-1	Commercial	SY100S366JC	Sn-Pb
SY100S366JCTR ⁽¹⁾	J28-1	Commercial	SY100S366JC	Sn-Pb
SY100S366JZ ⁽²⁾	J28-1	Commercial	SY100S366JZ with Pb-Free bar-line indicator	Matte-Sn
SY100S366JZTR ^(1, 2)	J28-1	Commercial	SY100S366JZ with Pb-Free bar-line indicator	Matte-Sn

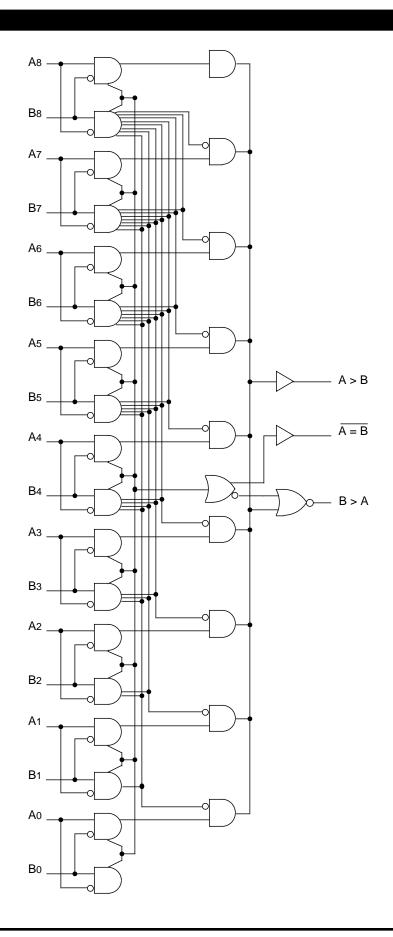
Notes:

1. Tape and Reel.

2. Pb-Free package is recommended for new designs.

28-Pin PLCC (J28-1)

BLOCK DIAGRAM



TRUTH TABLE⁽¹⁾

Inputs							Outputs				
A8B8	A7B7	A6B6	A5B5	A4B4	A3B3	A2B2	A1B1	AoBo	A > B	B > A	A = B
$\begin{array}{ccc} H & L \\ L & H \\ A8 = B8 \\ A8 = B8 \end{array}$	H L L H								H L H L		H H H H
$\begin{array}{rrrr} A8 = & B8 \\ A8 = & B8 \\ A8 = & B8 \\ A8 = & B8 \end{array}$	A7 = B7 A7 = B7 A7 = B7 A7 = B7	H L L H A6 = B6 A6 = B6	H L L H						H L H L	L H L	H H H H
A8 = B8 $A8 = B8$ $A8 = B8$ $A8 = B8$ $A8 = B8$	A7 = B7 A7 = B7 A7 = B7 A7 = B7	$A_6 = B_6$ $A_6 = B_6$ $A_6 = B_6$ $A_6 = B_6$	A5 = B5 A5 = B5 A5 = B5 A5 = B5	H L L H A4 = B4 A4 = B4	H L L H				НГΤΓ		нтт
$\begin{array}{rrrr} A8 = & B8 \\ A8 = & B8 \\ A8 = & B8 \\ A8 = & B8 \end{array}$	A7 = B7 A7 = B7 A7 = B7 A7 = B7	A6 = B6 A6 = B6 A6 = B6 A6 = B6	A5 = B5 A5 = B5 A5 = B5 A5 = B5	A4 = B4 A4 = B4 A4 = B4 A4 = B4	A3 = B3 A3 = B3 A3 = B3 A3 = B3	H L L H A2 = B2 A2 = B2	H L L H		H L H L	LHLH	ннн
$\begin{array}{rrrr} A8 = & B8 \\ A8 = & B8 \\ A8 = & B8 \end{array}$	A7 = B7 A7 = B7 A7 = B7	$\begin{array}{rrrr} A6 = & B6 \\ A6 = & B6 \\ A6 = & B6 \end{array}$	$A_5 = B_5$ $A_5 = B_5$ $A_5 = B_5$	$\begin{array}{rrrr} A4 = & B4 \\ A4 = & B4 \\ A4 = & B4 \end{array}$	A3 = B3 A3 = B3 A3 = B3	$A_2 = B_2$ $A_2 = B_2$ $A_2 = B_2$	$A_1 = B_1$ $A_1 = B_1$ $A_1 = B_1$	H L L H Ao = Bo	H L L	L H H	ннн

Note:

1. H = HIGH Voltage Level, L = LOW Voltage Level, Blank = X = Don't Care

DC ELECTRICAL CHARACTERISTICS

VEE = -4.2V to -5.5V unless otherwise specified; VCC = VCCA = GND

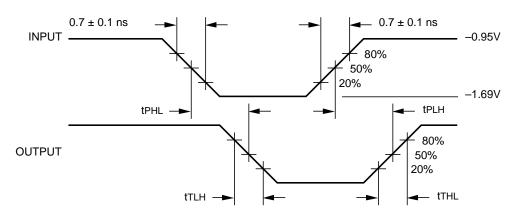
Symbol	pol Parameter		Тур.	Max.	Unit	Condition
Іін	Input HIGH Current, All Inputs			200	μA	VIN = VIH (Max.)
IEE	Power Supply Current	-120	-86	-60	mA	Inputs Open

AC ELECTRICAL CHARACTERISTICS

VEE = -4.2V to -5.5V unless otherwise specified; VCC = VCCA = GND

		TA = 0°C		TA = +25°C		TA = +85°C			
Symbol	Parameter	Min.	Max.	Min.	Max.	Min.	Max.	Unit	Condition
tPLH tPHL	Propagation Delay Data to Output	400	1500	400	1500	400	1500	ps	
ttlh tthl	Transition Time 20% to 80%, 80% to 20%	300	900	300	900	300	900	ps	

TIMING DIAGRAM

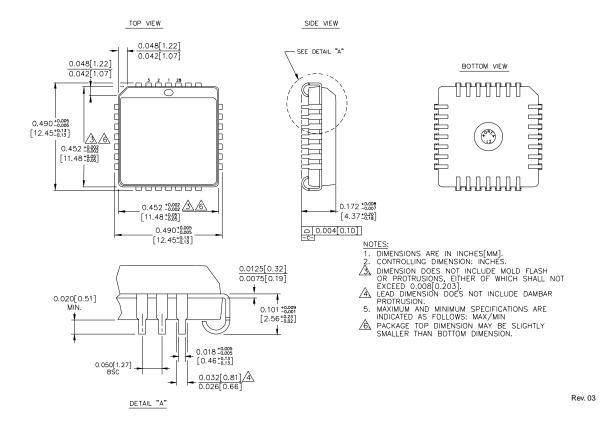


Propagation Delay and Transition Times

Note:

VEE = -4.2V to -5.5V unless otherwise specified; Vcc = VccA = GND

28-PIN PLCC (J28-1)



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